

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

A N	APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
	10051064	01/18/2002	438		2842	Isiedo
**APPLICANTS: Hamada Takashi; Murakami Satsohi; Yamazaki Shunpei; Nakamura Osamu; Kajiware Masayuki; Koezuka Junichi; Takayama Toru; 2823						
**CONTINUING DATA VERIFIED: none <i>SK</i>						
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-011085 01/19/2001 <i>SK</i> JAPAN 2001-022062 01/30/2001 <i>SK</i>						
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>			RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no			ATTORNEY DOCKET NO			
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no			SEL 299			
Verified and Acknowledged Examiners's Initials <i>SK</i>						
TITLE : Semiconductor device and method of manufacturing the same						
U.S. DEPT. OF COMM / PAT. & TM-PTO-436L (Rev. 12-94)						

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED		
		Assistant Examiner	Total Claims	
			Print Claim for Q.G	
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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